What is sequential consistency (SC)?

When thinking about concurrent programs, we implicitly assume what an execution can

look like. E.g. in the following program, it is unintuitive for us to see "O" printed.

$A = B = 0$ T_7
 $B = 42$ if $(A = 1)$
 $A = 1$ print(B) $A = 1$

If A was written, we expect B to have been written. This intuitive notion that we have of what executions are possible is exactly captured with SC. The exact assumptions we make under SC are:

1) Any execution behaves as if each operation had been executed sequentially (i.e. one after the other) by a single thread.

This assumption seems obvious (what else should happen?), but nevertheless needs to be stated

For example, this then directy implies that the only possible results of the concurrent writes $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$ are 1 or 2, and not something in between.

10) This equivalent sequential execution obeys program order (PO).

This then eliminates the unexpected execution above If the program execution was SC and T_2 printed " O ", then T_2 saw that $A=1$ was written, so the

equivalent sequential execution must have had

 $A = 1$ $R = 42$
Print(B)

 5 ince T_2 read

Now since $B = 42 \rightarrow A = 1$, the sequential execution must have been

 $B = 42$ $A = 1$ $|A| = |A|$ 10 p r i n t (8)

But if we execute these operations in isolation on a single thread, we obviously

print "42", a contradiction.

This is the same kind of reasoning we use when thinking about bad interleavings

or when we proved that the Peterson lock provides mutual exclusion.

Correctness

Now when implementing concurrent objects ourselves (e.g. a guere) the same question comes up of what should happen when multiple threads execute these operations concurrently. And again since it matches our intuition, we could use SC as our condition for whether an object behaves correctly. This means that if we implement a concurrent queue of that allows an execution of

\top_n T_1
qengly) 4 eng(x) $\vec{a} = \vec{q} \cdot \vec{d}$ eq()

where deg returns null or raises an exception because the gueve is empty, then we would consider this an incorrect implementation.

We would call our object "sequentially consistent", if in any possible execution,

the operations on a single instance this object are SC (and we ignore all

o ther operations).

*See the end of my notes on the lock-free unbounded queue for week 12 (https://n.ethz.ch/~aellison/pprog2023/week12/lock-free-queue.pdf) for the outline of an example
implementation that on the surface looks reasonable, but allows exactly such an execution.

<u>Ensuring</u> SC

Already with the Java memory model we saw that sequential consistency is not

always guaranteed. In the example from the beginning, it would be possible for

the program to print "0" in Java. Memory models however give us synchronization

tools to enforce sequential consistency (volatile, locks, etc.) when interacting

$with$ memory!

Assuming we know how to implement SC objects, the next question is, how can

we be sure that executions using multiple SC objects will be SC!

Will any execution where the behavior of each individual object is SC

automatically be SC as a whole? No This property would be called

composability" and SC does not satisfy this property.

Processors are a real example for this Despite having caches, (modern) processors

guarantee that all accesses to a single memory location will be sequentially

consistent (this is called "cache coherency" and is enforced using a cache

coherency protocol). So the following execution is not possible

$A=1$
print(A) // "2" $A=2$
print(A) // "1"

 T_{2}

 \Box

 T_{7}

(because if T_1 prints "2" we must have $A=1+A=2+print(A)$ (T_1) in the equivalent sequential execution and since $A=2\rightarrow print(A)(T_{2})$ τ_z cannot read A as"1".

The following, with 2 memory locations can homever happen

 $\frac{|\text{print}(A)}{|\text{print}(B)}$ / "2" | B = 7

 $A = B = \circ$

 τ_{1}

 \mathbb{T}_1

 T_n

The executions of the individual objects (shared variables) are ∞

 \prod_{α}

 \blacksquare

- $priut(B)/l''o''$ $B=1$ $p_{\text{riw}}+(A)$ // "2" | | $A=2$
- Equivalent sequential executions:

 \top_2

 $\frac{print(0)}{B=1}$ //"0" $A = 2$ ρ rint (A) π 2

 \Box

- but the execution as a whole is not. This could happen if T_2
- reordered the two writes.
- Another example using queves:
	- $19. erg(x)$ $10. erg(y)$
 $1 p. erg(x)$ $1 q. erg(y)$
 $1 p. inf(q. deg())$ $1 q. erg(y)$ $1 q. erg(y)$ $1 q.$

 $\overline{\mathcal{L}}$

Again, the executions of the individual objects (shared variables) are sc

q.enq(x)
print(q.deq()) //"y" q" (enq(y) p.enq(x) p.enq(y) p.enq(y) //"x"

 \Box

- Equivalent sequential executions:
	- q.enq(y)
q.enq(x)
print(q.deq()) //"y" $p.$ eng (\times) ρ .eng(y)
print(p. deg()) // "x"

while the entire execution is not. If $q \text{ deg}(t)$ gives "g", then we must have

q engly \rightarrow q engles in the equive seq. exec., but with PO this gives us $p.erg(y)$ og.eng (g) $\rightarrow q.erg(x)$ $\rightarrow p.erg(x)$, so deguering "x" from p would not be possible under SC.

This is undesirable. In the end we want all our executions to be SC in total, but so far SC object implementations alone are not enough to guarantee us this and it is unclear how we can achieve our goal. L inearizability

As often in math, when our definitions allow for undesitable cases, we try strengthening our assumptions. What went wrong in the previous example? Let's try inagining how the operations may have happened in time. Since 4 engly -0 9 english, we would $intuitively$ expect that "q.eng (x) " could not have completed be fore "q engly" started, so we imagine something like this: $\frac{q}{q} \frac{q}{\sqrt{q}}$ \top_1 $\frac{1}{\sqrt{2}}$

Other alternatives

Now if we consider when the other operations should have happened in PO: $\begin{array}{ccccc} & & q . e_0 q (x) & p . e_{0} q (x) & q . deg (x) \ \hline T_2 & & & q . e_{0} q (y) & 1 & p . deg (x) \ \hline \end{array}$ Then we would naturally expect that p. deq could only return "y",

since $p = a_1(y)$ must precede $p = a_1(x)$ in time. SC alone says nothing about fime, we never mentioned it in our definition. This means that under SC alone, even if peagles precedes peng(x), nothing in our assumptions imply that in the equivalent sequential execution, the operation peagon would still precede peng(x). Important: Normally, we don't care or think about time, because we don't know how our program will be scheduled. But here we see that if we also add this constraint using the real-time ordering of operations, it eliminates the

undesired behavior \overline{v}

let us define linearizability, by adding this new constraint 5ం $SC:$ $+$

call an execution linearizable, if: (\mathfrak{y}_e)

It is SC. (see above) ω

The equivalent sequential execution from SC also satisfies all (1)

real-fime orderings between operations, i.e. for any two or just $on-$

 o f them if operations σ_1 , σ_2 , if σ_1 precedes σ_2 in the real-time there are

execution, then σ_1 should precede σ_2 in the equivalent multiple.

sequential execution.

Another way to say this is, as in the book:

the moment it appears to "happen" and that determines where it is placed in the equivalent **Principle 3.5.1.** Each method call should appear to take effect instantaneously at some moment between its invocation and response. real time order preserved

We then call an object linearizable, if in an possible execution,

the operations on a single instance this object are linearizable.

 L inearizabilitz is in fact composable (proof in the book on page 57)

so if we can be sure that we use only linearizable objects,

any execution will be linearizable and thus SC; and we are h appy \bigcirc \bigcirc

<u>Linearizabilita vs. SC</u>

 $\left(4 \right)$

Π,

To be able to contrast lin. and SC, we need to include the time component of our executions. The following execution is for

example SC bat not lin: 9 en 9 (X)

 $4 \text{ deg}() \rightarrow y$ 9 en 9 (y)

Although it is unintuitive and harder to think of a queue

implementation that would work this way, this execution is SC,

Since we can choose the equivalent sequential execution

one could imagine each thread having a local baffer for engineers and threads $4.$ eng (y) q eng (x) first checking their local buffer when $g \, \text{deg}(U)$ // $\rightarrow y$ de & reueing.

obeging program order. We are allowed to reorder q_{0} and q englys because sc says nothing about time. Do NOT remember Sc by "allowed to reorder operations in time". The picture of SC to Keep in mind are the intaition/assumptions mentioned in the first section. Whenever you see Sc in other places (it is an important termo), they will be referring to these ideas and not histories where inter-thread order can be ignored. The real-time order between threads would only allow the sequential

q.eng(x) which is not possible, hence +
q.eng(y)
q.deq() // -> y execution is not linearizable. which is not possible, hence the execution

Linearization points: The last open question is how to show that an object is linearizable. The way to do this is by identifying "linearization points" in our program. These are points in our program that occur atomically/instantaneously and determine when the operation has "completed" or "faken effect" (but not yet returned). There is no precise definition of what "completed" or "taken effect" means. What the linearization points need to fullil is that if we order all operations by their linearization points intime, this gives us the (an) equivalent sequential execution, called the (a)¹¹ linearization¹¹ of the execution.