

⚠ DC-Analysis → neglect all small signals, AC analysis → neglect all large (bzw constant) signals

1 Basics

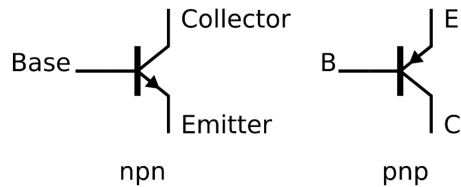
1.1 Constants

Elementary Charge	q	$1.602 \cdot 10^{-19}$	C
Thermal Voltage	$V_T = \frac{kT}{q}$	26	mV
Early Voltage	V_A	30...100	V
Saturation Current	I_C	$10^{-15} \dots 10^{-12}$	A
Current Gain	β	100...500	

1.2 Fundamentals

- **Resistor:** $u_R = Ri_R$
- **Capacitor:** $i_C = C \frac{du_C}{dt}$, $E = \frac{1}{2} CU^2$
- **Inductor:** $u_L = L \frac{di_L}{dt}$, $E = \frac{1}{2} LI^2$
- **Kirchhoff:** $\sum_{\text{Mesh}} U_k = 0$, $\sum_{\text{Node}} I_k = 0$
- **Thevenin:** $U_0 = U_{\text{Open}}$
- **Norton:** $I_0 = I_{\text{Short}}$

1.3 BJT



1.3.1 Large Signal $\hat{=}$ Operating point analysis

$NPN: I_C = I_{Se} e^{\frac{V_{BE}}{V_T}} \left(1 + \frac{V_{CE}}{V_A} \right)$
reverse saturation current of BJT. $\sim 10^{-15}$ to 10^{-12} A
 $\rightarrow 0$ when neglecting early voltage ($V_A \rightarrow \infty$)

$PNP: I_C = I_{Se} e^{-\frac{V_{BE}}{V_T}} \left(1 - \frac{V_{CE}}{V_A} \right)$

$I_B = \frac{I_C}{\beta}$
($I_B \rightarrow 0$ for $\beta \rightarrow \infty$)
 $\Rightarrow I_E = I_C$ when $\beta \rightarrow \infty$

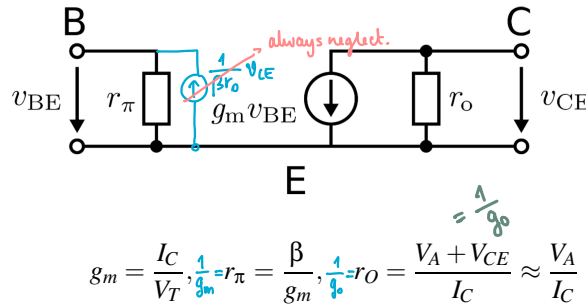
$I_E = (1 + \beta) I_B = \frac{1}{\alpha} I_C$

$\beta = \frac{\alpha}{1 - \alpha} \Leftrightarrow \alpha = \frac{\beta}{1 + \beta}$

$\int \frac{+}{-}$
Voltage drop always + to - positive.

- ⚠ if all inputs are constant → every signal in circuit is also constant!
- ⚠ Both MOSFET and BJT have SSE & have to find operating point.
- ⚠ KVL: always same φ to same φ ! (Best GND to GND).

1.3.2 Small Signal



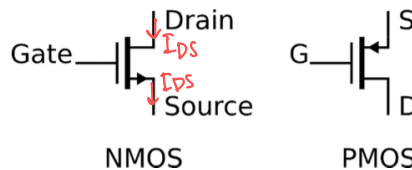
$g_m = \frac{I_C}{V_T} \cdot \frac{1}{\beta} = r_{\pi} = \frac{\beta}{g_m} \cdot \frac{1}{\beta} = r_o = \frac{V_A + V_{CE}}{I_C} \approx \frac{V_A}{I_C}$

1.3.3 Modes

Here for NPN, for PNP all operators flipped.

Mode	Voltage	Current
Active	$V_{BE} > 0, V_{CB} > 0$	$I_C = I_{Se} e^{\frac{V_{BE}}{V_T}}$
Saturation	$V_{BE} > 0, V_{CB} \leq 0$	$I_C = -I_S$
Cutoff	$V_{BE} \leq 0, V_{CB} > 0$	$I_C = 0$

1.4 MOSFET



1.4.1 Large Signal → neglect all small signals!

$NMOS: I_D = \frac{K'}{2} \frac{W}{L} (V_{GS} - V_t)^2 (1 + \lambda V_{DS})$

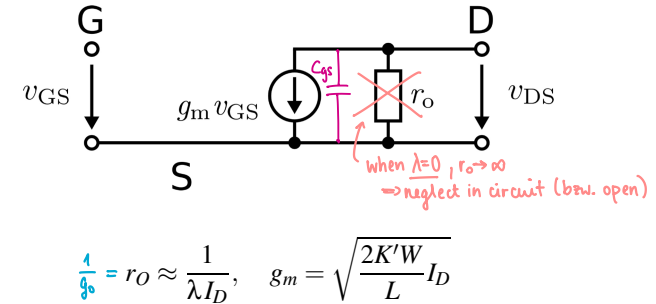
$PMOS: I_D = -\frac{K'}{2} \frac{W}{L} (V_{GS} - V_t)^2 (1 - \lambda V_{DS})$

⚠ $I_G = 0$ @ DC (in saturation mode)

MOSFET Parameters:

- K' : Intrinsic transconductance coefficient
- V_t : Threshold voltage
- W/L : Gate width/channel length (physical properties of the component!)
- λ : Characteristic length (describes the channel length modulation. $\lambda \approx 0 \Rightarrow$ we neglect the channel length modulation.)

1.4.2 Small Signal → neglect all large signals!



$\frac{1}{g_o} = r_o \approx \frac{1}{\lambda I_D}$, $g_m = \sqrt{\frac{2K'W}{L} I_D}$

→ Flip horizontally for PMOS

1.4.3 Modes

Here for NMOS, for PMOS all operators flipped.

Mode	Voltage	Current
Cutoff	$V_{GS} < V_t$	$I_D = 0$
Linear	$V_{DS} \leq V_{GS} - V_t$	$I_D = \frac{K'}{2} \frac{W}{L} (2(V_{GS} - V_t)V_{DS} - V_{DS}^2)$
Saturation	$V_{DS} > V_{GS} - V_t$	$I_D = \frac{K'}{2} \frac{W}{L} (V_{GS} - V_t)^2$

1.5 Operating Point Analysis

Use large signal equations and Kirchhoff's laws.

- $L \rightarrow$ short circuit
- $C \rightarrow$ open circuit
- Replace small signal sources

1.6 Small Signal Equivalent

For small signals the transistors behave almost linearly and small signal models can be used.

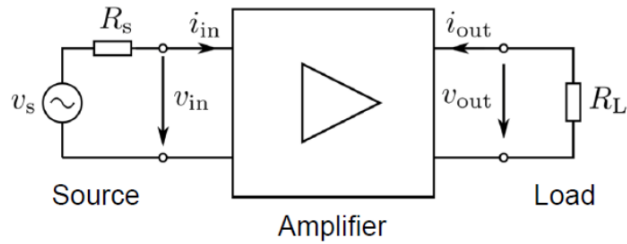
1. Replace all BJTs and MOSFETs with their SSE
2. Keep all the passive elements
3. Replace constant voltage and current sources
 $U_{DC} \rightarrow$ short circuit
 $I_{DC} \rightarrow$ open circuit
4. Label elements, voltages and currents
5. Connect nodes with equal potential

Both BJT & MOSFET:

- have SSE
- have to find operating point

2 Single Transistor Amplifiers

2.1 Two-Port Network



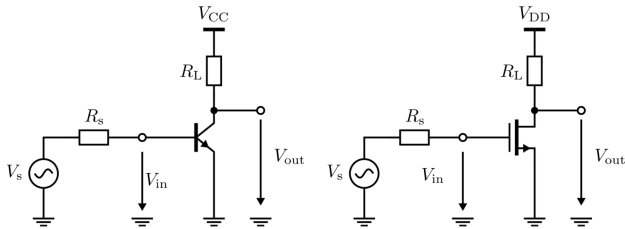
Impedance $Z_{in} = \frac{v_{in}}{i_{in}} \Big|_{v_{out}=0}$ $Z_{out} = \frac{v_{out}}{i_{out}} \Big|_{v_s=0}$

Gain $A_V = \frac{v_{out}}{v_s} \Big|_{i_{out}=0}$ $A_I = \frac{i_{out}}{i_s} \Big|_{v_{out}=0}$

Transconductance $G_m = \frac{i_{out}}{v_{in}} \Big|_{v_{out}=0}$

Resistance $R = \frac{v_{out}}{i_{in}} \Big|_{i_{out}=0}$

2.2 Common-Emitter/Source Amplifier



BJT (C.Emitter)

$$R_{in} = \frac{v_{in}}{i_{in}} = r_{\pi}$$

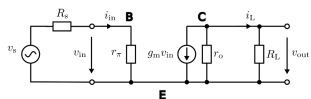
$$R_{out} = \frac{v_{out}}{i_{out}} = r_o$$

$$A_V = \frac{v_{out}}{v_s} = -g_m(r_o || R_L) \frac{r_{\pi}}{r_{\pi} + R_S}$$

$$\approx -g_m R_L \frac{r_{\pi}}{r_{\pi} + R_S}$$

$$A_I = \frac{R_S}{R_S + r_{\pi}} g_m r_{\pi}$$

$$= \frac{R_S}{R_S + r_{\pi}} \beta$$



MOSFET (C.Source)

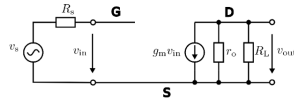
$$R_{in} = \infty$$

$$R_{out} = r_o$$

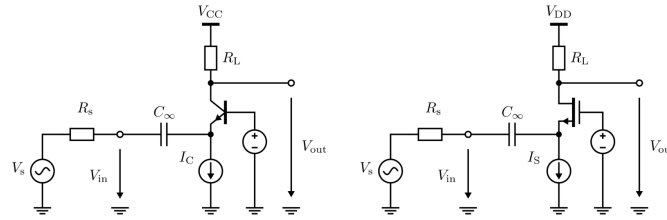
$$A_V = \frac{v_{out}}{v_s} = \frac{v_{out}}{v_{in}} = -g_m(r_o || R_L)$$

$$\approx -g_m R_L$$

$$A_I = \infty$$



2.3 Common-Base/Gate Amplifier



BJT (C.Base)

$$R_{in} = \frac{v_{in}}{i_{in}} = \frac{1}{\frac{1}{r_{\pi}} + \frac{1}{r_o} + g_m}$$

$$\approx \frac{1}{g_m}$$

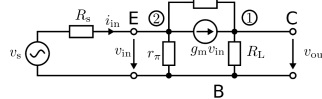
$$R_{out} \approx ((r_{\pi} || R_S) g_m + 1) r_o$$

$$\approx \beta r_o$$

$$A_V = \frac{v_{out}}{v_s} \approx \frac{R_{in}}{R_S + R_{in}} g_m R_L$$

$$\approx \frac{g_m R_L}{1 + g_m R_S}$$

$$A_I = \frac{i_{out}}{i_s} = -\frac{g_m}{\frac{1}{R_S + R_{in}} + g_m}$$



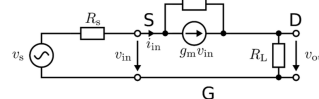
MOSFET (C.Gate)

$$R_{in} \approx \frac{1}{g_m}$$

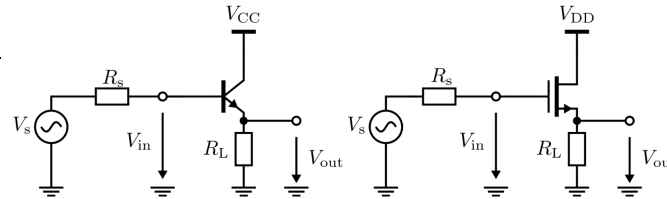
$$R_{out} \approx (1 + g_m R_S) r_o$$

$$A_V = \frac{v_{out}}{v_s} \approx \frac{g_m R_L}{1 + g_m R_S}$$

$$A_I = \frac{i_{out}}{i_s} = -1$$



2.4 Common-Collector/Drain Amplifier



BJT (C.Collector)

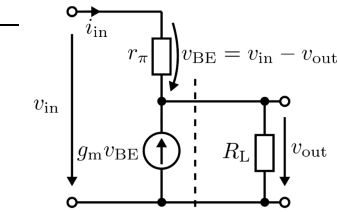
$$R_{in} = r_{\pi} + (1 + \beta) R_L$$

$$R_{out} = \frac{1}{g_m + \frac{1}{r_{\pi}}} \approx \frac{1}{g_m}$$

$$\approx \beta r_o$$

$$A_V \approx \frac{1}{1 + \frac{1}{g_m R_L}}$$

$$A_I = 1 + \beta$$



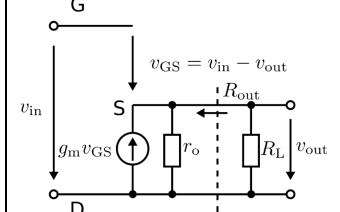
MOSFET (C.Drain)

$$R_{in} = \infty$$

$$R_{out} = \frac{1}{g_m}$$

$$A_V \approx \frac{1}{1 + \frac{1}{g_m R_L}}$$

$$A_I = \infty$$



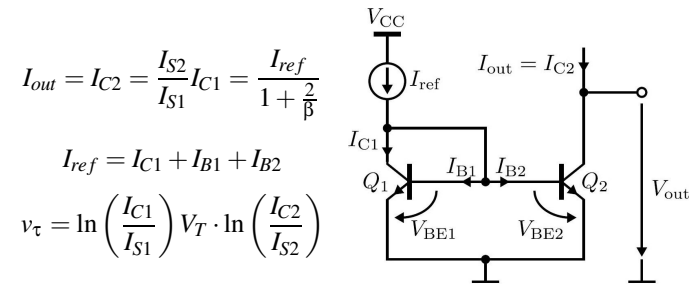
2.5 Comparison

	CE/CS	CC/CD	CB/CG
Voltage Gain	$\ll -1$	≈ 1	$\gg 1$
Current Gain	$\ll -1$	Moderate	≈ -1
R_{in}	High	High	Low
R_{out}	High	Low	High

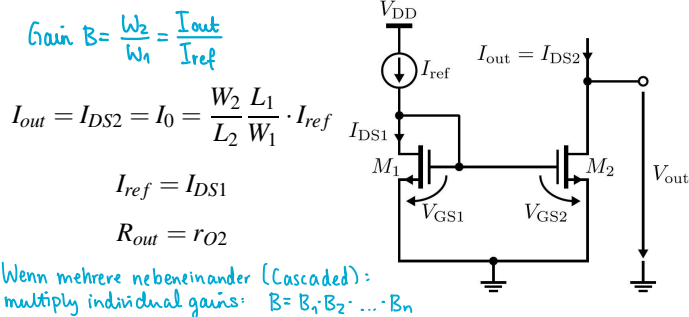
This makes CC/CD good voltage sources, while CB/CG are good current sources.

3 Current Mirrors

3.1 BJT



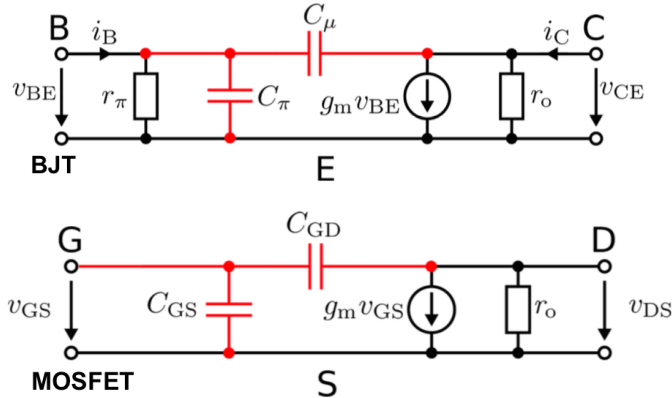
3.2 MOSFET



4 Frequency Response of Amplifiers

4.1 Parasitic Capacitances

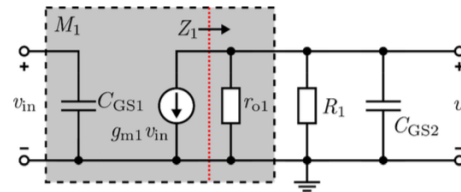
Capacitive effects between the terminals may have to be considered for frequency analysis.



4.2 Multistage Amplifier

Single transistor amplifiers have limited gain range. Multi-stage amplifiers help to spread the gain per stage.
 $|A_v(j\omega)|$ = Amplification $\phi_v(j\omega)$ = Phase shift

4.2.1 Calculation of 1st Stage



$$v_1 \approx -\frac{g_{m1} R_1}{1 + s C_{GS2} R_1} v_{in}$$

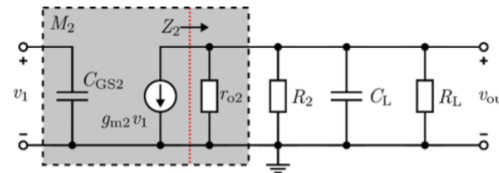
$$v_{out} = -\frac{g_{m2} R_2}{1 + s C_L R_2} v_1 = \frac{g_{m2} R_2}{1 + s C_L R_2} \frac{g_{m1} R_1}{1 + s C_{GS2} R_1} v_{in}$$

For a sinusoidal signal with angular frequency ω :

$$|A_v(j\omega)| = \frac{g_{m2} R_2 \cdot g_{m1} R_1}{\sqrt{1 + (\omega C_L R_2)^2} \sqrt{1 + (\omega C_{GS2} R_1)^2}}$$

$$\phi_v(j\omega) = -\tan^{-1}(\omega C_{GS2} R_1) - \tan^{-1}(\omega C_L R_2)$$

4.2.2 Calculation of 2nd Stage



$$v_{out} = -\frac{g_{m2} R_2}{1 + s C_L R_2} v_1$$

$$A_{v2}(s) = \frac{v_{out}(s)}{v_1(s)} = -\frac{g_{m2} R_2}{1 + s C_L R_2}$$

For a sinusoidal signal with angular frequency ω

$$|A_{v2}(j\omega)| = \frac{g_{m2} R_2}{\sqrt{1 + (\omega C_L R_2)^2}}$$

$$\phi_{v2}(j\omega) = -180^\circ - \tan^{-1}(\omega C_L R_2)$$

4.3 Bode Plots

	Amplitude	Phase
Constant	20dB log K	0° if K ≥ 0, 180° else
Negative Pole	-20dB/dec	-90° over 2 dec
Positive Pole	-20dB/dec	+90° over 2 dec
Negative Zero	+20dB/dec	+90° over 2 dec
Positive Zero	+20dB/dec	-90° over 2 dec
Zero at Origin	+20dB/dec	+90°
Pole at Origin	-20dB/dec	-90°

For voltage and current use: $20 \log_{10}(|A(j\omega)|)$

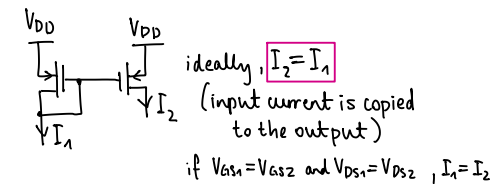
For power use: $10 \log_{10}(|A(j\omega)|)$

Resistor: A=const, $\phi = 0^\circ$

Capacitor: A=-20dB/dec, $\phi = -90^\circ$

Inductor: A=+20dB/dec, $\phi = +90^\circ$

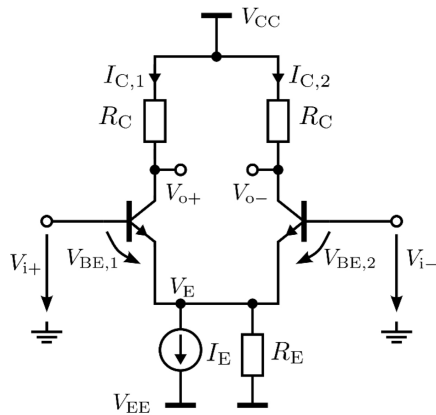
Unity Current Mirror:



5 Differential Amplifiers

5.1 Differential Amplifiers

5.1.1 BJT



General:

$$v_{icm} = \frac{V_{i+} + V_{i-}}{2}$$

$$v_{id} = v_{i+} - v_{i-}$$

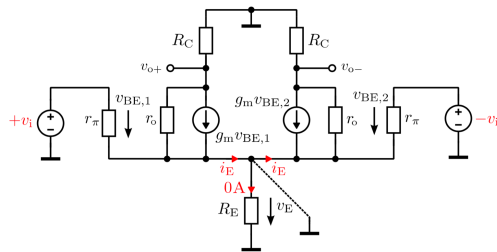
$$v_{od} = v_{o+} - v_{o-}$$

$$V_{i\pm} = v_{icm} \pm \frac{1}{2}v_{id}$$

Differential Mode: ($v_{i+} = -v_{i-} = v_{id}$)

$$v_{o+} = -v_{o-} = v_{od}$$

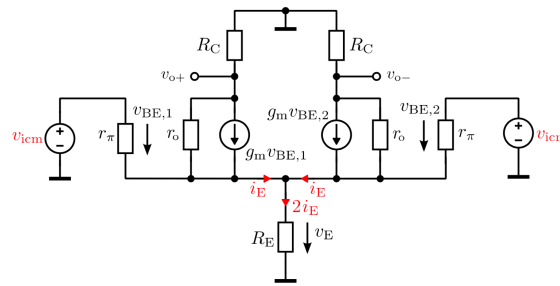
$$A_{vd} = \frac{v_{od}}{v_{id}} = -g_m R_C$$



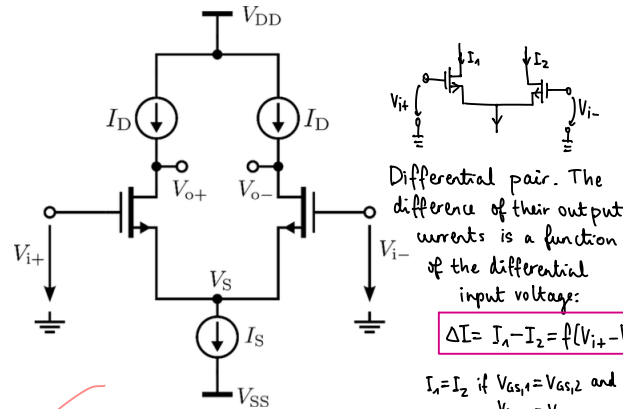
Common Mode: ($v_{i+} = v_{i-} = v_{icm}$)

$$v_{o+} = v_{o-} = v_{ocm}$$

$$A_{vcm} = \frac{v_{ocm}}{v_{icm}} = -\frac{R_C}{2R_E}$$



5.1.2 MOSFET



Differential pair. The difference of their output currents is a function of the differential input voltage:

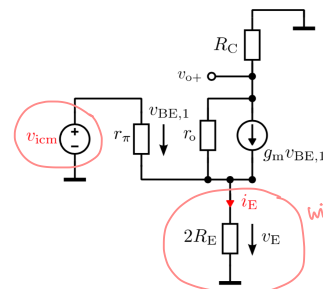
$$\Delta I = I_1 - I_2 = f(V_{i+} - V_{i-})$$

$$I_1 = I_2 \text{ if } V_{GS,1} = V_{GS,2} \text{ and } V_{DS,1} = V_{DS,2}$$

$$A_{vd} = -g_m r_o \gg -g_m R_L$$

5.2 Half Circuit *Idea: Circuit symmetrical: only analyse*

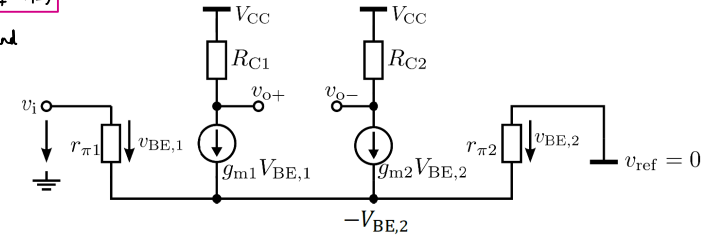
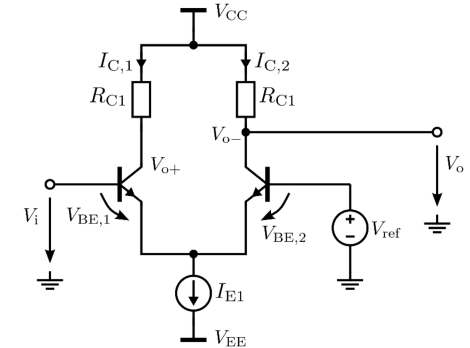
5.2.1 Common Mode *half of circuit! Other half behaves identically!*



5.2.2 Differential Mode

Only possible if virtual ground at the emitter.

5.3 Single ended Differential Amplifier



$$v_{o+} = -\frac{g_{m1}R_{C1}}{2}, \quad v_{o-} = \frac{g_{m2}R_{C1}}{2}, \quad v_{id} = \frac{v_{i+}}{2}$$

$$g_{m1} = g_{m2} = g_m, \quad R_{C1} = R_{C2} = R_C$$

$$A_{vd} = \frac{v_{od}}{v_i} = -g_m R_C$$

5.4 Common-Mode Rejection Rate

How strong a common mode signal is attenuated compared to a differential signal.

$$G = \frac{A_{vd}}{A_{vcm}} = \frac{-g_m R_C}{-\frac{R_C}{2R_E}} = 2g_m R_E$$

$$\text{In dB: } CMRR = G_{dB} = 20 \cdot \log_{10} G$$

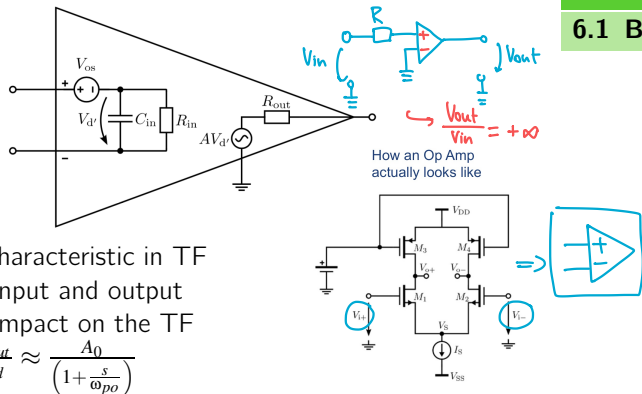
5.5 Operational Amplifiers

5.5.1 Ideal OpAmp

- No common mode gain
- Infinite input impedance
- Zero output impedance
- Infinite differential open loop gain
- Infinite bandwidth
- Infinite CMRR
- $V_{out} = A_V(V_{i+} - V_{i-}) = A_V \cdot V_D$

any kind of
mit Rückkopplung:
 $V_{ud} = 0$

5.5.2 Non-ideal OpAmp:

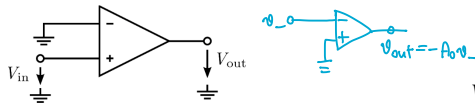


- Lowpass characteristic in TF
- Offset at input and output
- Load has impact on the TF
- $A_V(s) = \frac{V_{out}}{V_d} \approx \frac{A_0}{(1 + \frac{s}{\omega_{po}})}$

5.6 OpAmp Basic Circuits

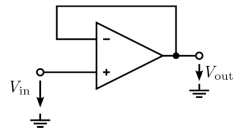
Voltage Comparator

$$V_{out} = \text{sign}(V_{in}) \cdot V_{cc}$$



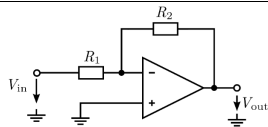
Voltage Follower

$$V_{out} = V_{in}$$



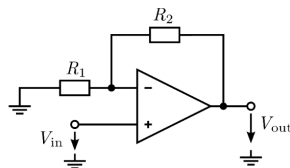
Inverting

$$V_{out} = -\frac{R_2}{R_1} \cdot V_{in}$$



Non-Inverting

$$V_{out} = \left(1 + \frac{R_2}{R_1}\right) V_{in}$$



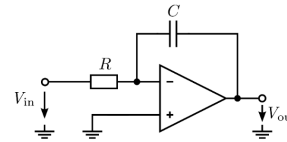
non inverting

Integrator

$$V_{out} = + \frac{V_{in}(s)}{sRC}$$

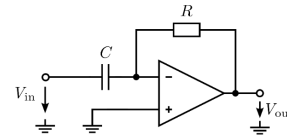
$$= V_{out}(0) - \frac{1}{RC} \int_0^t V_{in}(\tau) d\tau$$

Phase -90° for all frequencies



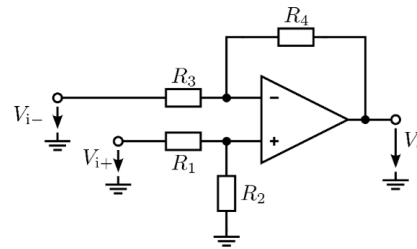
Diffiator

$$V_{out} = V_{in}(s)sRC = -RC \frac{dV_{in}(t)}{dt}$$



6 Instrumentation Amplifiers

6.1 Basic Instrumental Amp

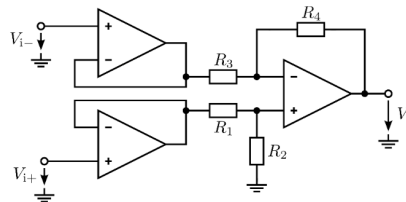


$$V_0 = \frac{R_2}{R_1} \frac{1 + \frac{R_4}{R_3}}{1 + \frac{R_2}{R_1}} V_{i+} - \frac{R_4}{R_3} V_{i-}$$

$$R_1 = R_3, R_2 = R_4$$

$$V_0 = G \cdot V_{i+} - G \cdot V_{i-}, \quad G = \frac{R_4}{R_3} = \frac{R_2}{R_1}$$

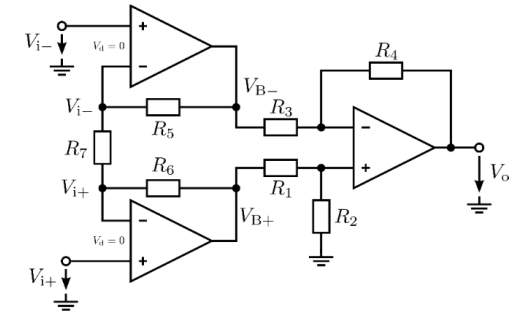
6.2 Buffered Instrumental Amp



$$V_0 = V_{icm} \left(\frac{R_2(R_3 + R_4)}{R_3(R_1 + R_2)} - \frac{R_4}{R_3} \right) + \frac{V_{id}}{2} \left(\frac{R_2(R_3 + R_4)}{R_3(R_1 + R_2)} + \frac{R_4}{R_3} \right)$$

$$CMRR = \frac{A_d}{A_{cm}} = \frac{V_0/V_{id}}{V_0/V_{icm}} = \frac{R_2(R_3 + R_4) + R_4(R_1 + R_2)}{2(R_2R_3 - R_4R_1)}$$

6.3 Input Stage Gain



Input Stage: (Differential and common mode gain)

$$A_B = \frac{V_{BD}}{V_{id}} = \frac{V_{B+} - V_{B-}}{V_{i+} - V_{i-}} = \frac{R_5 + R_6}{R_7}$$

$$A_{cm,B} = \frac{V_{B+} + V_{B-}}{V_{i+} + V_{i-}} = 1$$

(No current through R_5, R_6, R_7)

Total: (Differential and common mode gain)

$$A_d' = \frac{V_O}{V_{id}} = \frac{A_B}{2} \left(\frac{R_2(R_3 + R_4)}{R_3(R_1 + R_2)} + \frac{R_4}{R_3} \right)$$

$$A_{cm} = A_{cm,B} \left(\frac{R_2(R_3 + R_4)}{R_3(R_1 + R_2)} - \frac{R_4}{R_3} \right)$$

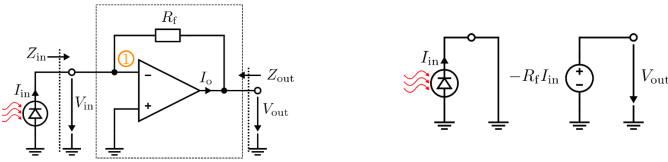
$$CMMR = \frac{A_d'}{A_{cm}} = A_B \frac{A_d}{A_{cm}}$$

→ CMRR increased by factor A_B due to an input stage!

7 Various Amplifiers

7.1 Transimpedance Amplifier

$$V_{in} \approx 0, \quad Z_{in} \approx 0, \quad Z_{out} \approx 0$$



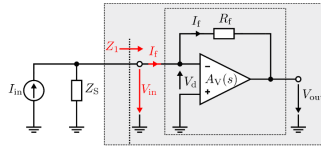
Frequency Response I

$$Z_S = (R_S || (C_S + C_{in}))$$

$$Z_1(s) = \frac{R_f}{1 + A_V(s)}$$

$$I_f = \frac{V_{in} + A_V V_{in}}{R_f}$$

$$Z_{T'} = -A_V(s) Z_1 \approx \frac{-R_f}{1 + A_0 \omega p_0}$$



Frequency Response II

$$Z_{in} = (Z_S || Z_1)$$

$$Z_T = \frac{V_{out}}{I_{in}}$$

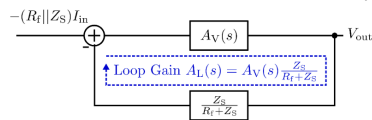
$$I_f = I_{in} - \frac{V_{in}}{Z_S} I_{in} - \frac{I_f Z_1}{Z_S}$$

$$V_{out} = -A_V(s) Z_1 I_f = -\frac{Z_1 Z_S}{Z_1 + Z_S} A_V(s) I_{in}$$

$$Z_T(s) = -(R_f || Z_S) \frac{A_V(s)}{1 + A_V(s) \frac{Z_S}{R_f + Z_S}}$$

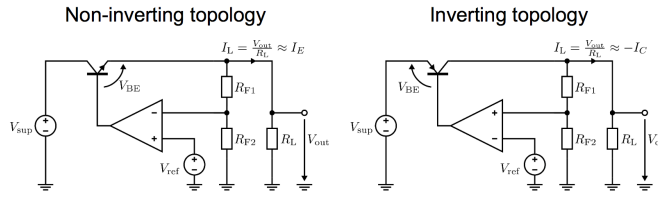
$$\text{Loop Gain } A_L(s) = A_V(s) \frac{Z_S}{R_f + Z_S}$$

$$\text{Feedback factor } \beta(s) = \frac{Z_S}{R_f + Z_S}$$



→ Higher transimpedance gain results in lower bandwidth

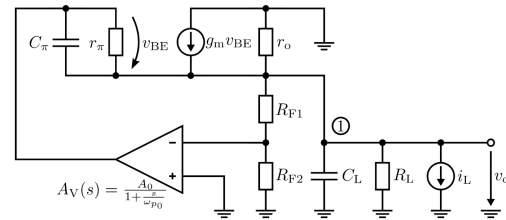
7.2 Linear Voltage Regulator



$$R_{F1}, R_{F2} \gg R_L \Rightarrow I_L \approx \frac{I_E}{-I_C}$$

$$V_{out} = \left(1 + \frac{R_{F1}}{R_{F2}}\right) V_{ref}, \quad A'_0 = A_0 \frac{R_{F2}}{R_{F1} + R_{F2}}$$

7.2.1 Small Signal Equivalent



$$Z_{out} = \frac{v_{out}}{i_o} = \frac{1}{g_m A'_0} \left(1 + \frac{s}{\omega p_0 A'_0}\right) \cdot \left(1 + \frac{s C_L}{g_m}\right)$$

7.3 Logarithmic Amplifiers

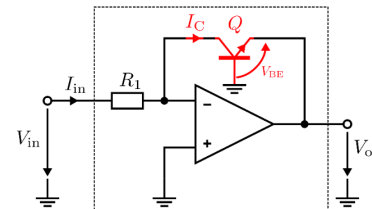
Non-linear circuit with output voltage proportional to the logarithm/exponential of the input voltage.

7.3.1 Logarithmic Amplifier

$$V_{in} > 0$$

$$I_{in} = \frac{V_{in}}{R_1} = I_C = I_S e^{\frac{V_{BE}}{V_T}} = I_S e^{\frac{V_{out}}{V_T}}$$

$$V_{out} = -V_T \cdot \ln\left(\frac{I_{in}}{I_S}\right) = -V_T \cdot \ln\left(\frac{V_{in}}{R_1 I_S}\right)$$



Resonance: (may appear in Systems with two or more poles).

$$G(s) = \frac{K \omega_n^2}{s^2 + 2\zeta \omega_n s + \omega_n^2}$$

ω_n : Natural frequency
 ζ : damping ratio
 K : gain
 $\omega_n = \sqrt{\frac{k}{m}}$, $\zeta = \frac{d}{2\sqrt{km}}$
 $K = -\frac{1}{k}$

frequency response: $G(j\omega) = \frac{K \omega_n^2}{(\omega_n^2 - \omega^2) + j(2\zeta \omega_n \omega)}$

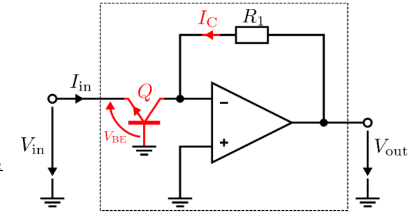
$|G(j\omega)| = \frac{K \omega_n^2}{\sqrt{(\omega_n^2 - \omega^2)^2 + (2\zeta \omega_n \omega)^2}}$, $\angle G(j\omega) = -\arctan\left(\frac{2\zeta \omega_n \omega}{\omega_n^2 - \omega^2}\right)$

7.3.2 Anti-logarithmic Amplifier

$$V_{in} < 0$$

$$I_C = I_S e^{\frac{V_{BE}}{V_T}} = I_S e^{\frac{-V_{in}}{V_T}}$$

$$V_{out} = I_C R_1 = I_S R_1 e^{-\frac{V_{in}}{V_T}}$$



8 Filters

TF: Transfer Function

$$\text{AR: Amplitude Response } |T(j\omega)| = \frac{|V_0(j\omega)|}{|V_i(j\omega)|}$$

$$\text{PR: Phase Response } \angle T(j\omega) = \angle \frac{V_0(j\omega)}{V_i(j\omega)}$$

8.1 Damping factor

$$\zeta = \frac{\omega p_0 + \omega p_s}{2\sqrt{A_0 \omega p_0 \omega p_s}}$$

- Overdamped:** $\zeta > 1, Q < \frac{1}{2}$
→ solutions of characteristic equation: $p_{1,2} \in \mathbb{R}, < 0$
- Critically damped:** $\zeta = 1, Q = \frac{1}{2}$
→ identical solutions: $p_1 = p_2 = -\omega_n$
- Underdamped:** $0 < \zeta < 1, Q > \frac{1}{2}$
→ complex conjugates: $p_{1,2} = -\zeta \omega_n \pm j \omega_n \sqrt{1 - \zeta^2}$

8.2 Standard Transfer Function

$$H(s) = \frac{N(s)}{\frac{s^2}{\omega_n^2} + \frac{s}{Q\omega_n} + 1}$$

$N(s) = k$: lowpass filter with DC-Gain: k

$N(s) = k \frac{s^2}{\omega_n^2}$: highpass filter with high frequency gain k

$N(s) = k \frac{s}{Q\omega_n}$: bandpass filter with max gain k

$N(s) = k \left(1 - \frac{s^2}{\omega_n^2}\right)$: notch filter with gain k

- For Stability, need $\zeta \geq 0$
- For $\zeta \geq 1$ poles real (overdamped system)
- For $\zeta = 1$ poles real & equal (critical damping)
- For $0 < \zeta < 1$ poles complex (under-damped system)
- For $\zeta = 0$ poles imaginary (undamped system)
- For $\zeta \geq 1/\sqrt{2}$ magnitude bode plot decreasing in ω
- For $0 \leq \zeta < 1/\sqrt{2}$ magnitude bode plot has a max. at $\omega = \omega_n \sqrt{1 - 2\zeta^2}$ and $|G(j\omega)| = \frac{K}{2\zeta \sqrt{1 - \zeta^2}}$

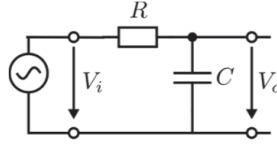
8.3 First Order Passive Filters

8.3.1 Low-Pass Filter

TF: $T(s) = \frac{V_0}{V_i} = \frac{1}{1+sRC}$

AR: $\frac{|V_0(j\omega)|}{|V_i(j\omega)|} = \frac{1}{\sqrt{1+(\omega RC)^2}}$

PR: $\angle \frac{V_0(j\omega)}{V_i(j\omega)} = -\arctan(\omega RC)$

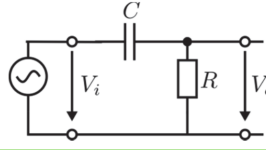


8.3.2 High-Pass Filter

TF: $T(s) = \frac{V_0}{V_i} = \frac{sRC}{1+sRC}$

AR: $\frac{|V_0(j\omega)|}{|V_i(j\omega)|} = \frac{\omega RC}{\sqrt{1+(\omega RC)^2}}$

PR: $\angle \frac{V_0(j\omega)}{V_i(j\omega)} = 90 - \tan^{-1}(\omega RC)$

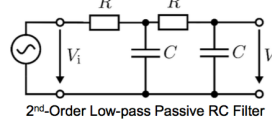


8.4 Second Order Passive Filters

8.4.1 General

TF: $T(s) = \frac{1}{s^2 + \frac{1}{Q_0}s + \omega_0^2}$

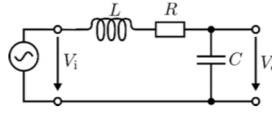
$D(s) = s^2 + \frac{\omega_0}{Q_0}s + \omega_0^2$



8.4.2 Low-Pass Filter

TF: $T(s) = \frac{1}{s^2 + \frac{1}{Q_0}s + \omega_0^2}$

$= \frac{K\omega_0^2}{s^2 + \frac{\omega_0}{Q_0}s + \omega_0^2}$



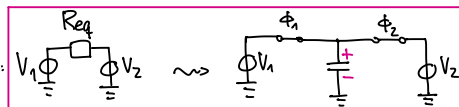
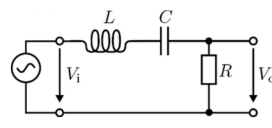
8.4.3 High-Pass Filter

TF: $T(s) = \frac{s^2 K}{s^2 + \frac{\omega_0}{Q_0}s + \omega_0^2}$

8.4.4 Band-Pass Filter

TF: $T(s) = \frac{s^{\frac{R}{L}}}{s^2 + \frac{R}{L}s + \frac{1}{LC}}$

$= \frac{sK\frac{\omega_0}{Q_0}}{s^2 + \frac{\omega_0}{Q_0}s + \omega_0^2}$



↳ Vorgehen: ①

②

③

④

⚠ Wenn es steht " ... $\phi_1 \rightarrow \phi_2$... " : analyse circuit in stage ϕ_2 !

8.4.5 Comparison with First Order Passive Filter

Passive Filters	Active Filters
2 passive elements that determine pole	3 passive elements, pole determined by elements in feedback
Fixed gain of 1	Variable pass band gain
No power consumption	OpAmp consumes power
Real filter transfer function close to ideal	Real filter transfer function dependent on gain

8.5 First Order Active Filters

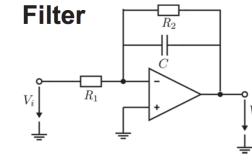
8.5.1 Low-Pass Filter

TF: $T(s) = -\frac{R_2}{R_1} \frac{1}{1+sR_2C}$

AR: $|T(j\omega)| = \frac{R_2}{R_1} \frac{1}{\sqrt{1+(\omega R_2C)^2}}$

PR: $\angle T(j\omega) = 180 - \tan^{-1}(\omega RC)$

Filter



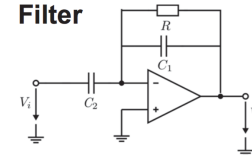
8.5.2 High-Pass Filter

TF: $T(s) = \frac{V_0}{V_i} = -\frac{sRC_2}{1+sR_2C}$

AR: $|T(j\omega)| = \frac{R_2}{R_1} \frac{\omega RC_2}{\sqrt{1+(\omega RC_1)^2}}$

PR: $\angle T(j\omega) = -90 - \tan^{-1} \omega RC_1$

Filter

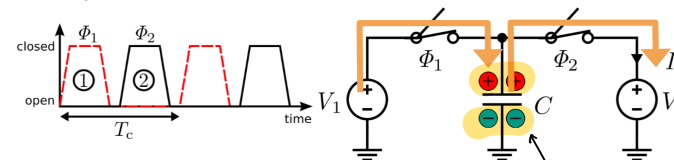


9 Switched Capacitor Filters Bed: $f_{vin} \ll f_c$

Resistors take up too much space, so replace them by switched capacitors.

9.1 Equivalent Resistor

We transfer charge ΔQ from potential V_1 to V_2 at a fixed rate $f_c = \frac{1}{T_c}$

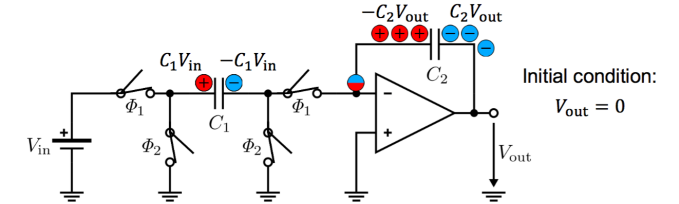


$$\Delta Q = C(V_1 - V_2) \quad Q = C \cdot V$$

$$I_{2,avg} = \frac{\Delta Q}{T_c}, \quad R_{eq} = \frac{T_c}{C} = \frac{1}{f_c C}$$

define yourself @ beginning which plate is positive and negative & keep it throughout the whole task!

9.2 Inverting Integrator



Phase 1: ϕ_1 on charge accumulates on C_1 and C_2

$Q_1 = C_1 \cdot V_{in}(n-1)$

$Q_2 = -C_2 \cdot V_{out}(n-1)$

Phase 2: ϕ_2 on C_1 is discharged

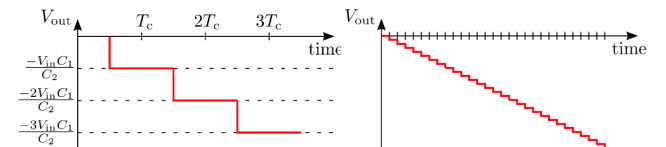
$Q_1 = 0$

$Q_2 = -C_2 \cdot V_{out}(n-0.5)$

$C_2 V_{out}(n) = C_2 V_{out}(n-0.5)$

$C_2 V_{out}(n) = C_2 V_{out}(n-1) - C_1 V_{in}(n)$

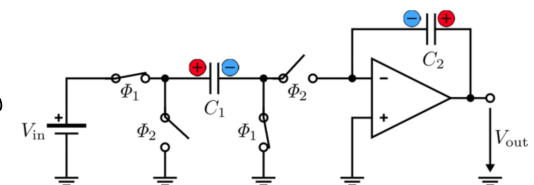
$\frac{V_{out}}{V_{in}} = -\frac{C_1}{C_2} \frac{1}{1-z^{-1}}$



→ Seems continuous for small T_c

$V_{out}(nT_c) = -\frac{C_2}{T_c C_2} \int_0^{nT_c} V_{in}(t) dt$

9.3 Non-Inverting Integrator



Same circuit as before, but change of switching circuit
→ charge on C_2 is inverted

Phase 1: ϕ_1 on C_1 is charged to V_{in}

$$Q_1 = C_1 \cdot V_{in}(n-1)$$

$$Q_2 = C_2 \cdot V_{out}(n-1)$$

Phase 2: ϕ_2 on Charge is transferred to C_2

$$Q_1 = 0$$

$$Q_2 = C_2 \cdot V_{out}(n-0.5)$$

$$C_2 V_{out}(n) = C_2 V_{out}(n-0.5)$$

$$C_2 V_{out}(n) = C_2 V_{out}(n-1) + C_1 V_{in}(n-1)$$

$$\frac{V_{out}}{V_{in}} = \frac{C_1}{C_2} \frac{z^{-1}}{1-z^{-1}}$$

9.4 Procedure

1. Select a polarity for each capacitor
2. Draw circuit diagrams for each phase
3. Determine charge of each capacitor during each phase ($Q_k = C_k V_k$)
4. Identify nodes where charge conservation can be applied
5. Derive charge equations at phase transitions using polarity of the phase to come
6. Z-Transform for transfer function

9.5 Z-Transform

- **Definition:** $Z\{x[nT_c]\} = X(z) = \sum_{k=-\infty}^{\infty} x[kT_c] z^{-k}$
- **Time Delay:** $Z\{x[(n-k)T_c]\} = z^{-k} X(z)$
- **Integration:** $\frac{T_c}{1-z^{-1}}$
- **Differentiation:** $\frac{1-z^{-1}}{T_c}$
- **Mapping to $j\omega$ -axis:** $z = e^{j\omega T_c} = e^{j\frac{2\pi f}{f_c} T_c}$
- **Mapping to s -axis:** $s = \frac{z-1}{T_c}$, or $1-z^{-1} = sT_c$ (equivalent)
↳ forward euler transformation

9.6 Transient Response

Capacitor:

$$u_C = u_C(t \rightarrow \infty) - [u_C(t \rightarrow \infty) - u_C(t = t_0)] e^{-\frac{t-t_0}{RC}}$$

$$i_C = \frac{1}{R} [u_C(t \rightarrow \infty) - u_C(t = t_0)] e^{-\frac{t-t_0}{RC}}$$

Inductor:

$$u_L = R[i_L(t \rightarrow \infty) - i_L(t = t_0)] e^{-\frac{R}{L}(t-t_0)}$$

$$i_L = i_L(t \rightarrow \infty) - [i_L(t \rightarrow \infty) - i_L(t = t_0)] e^{-\frac{R}{L}(t-t_0)}$$

10 Appendix

10.1 Passive Elements

[R] Seriell: $R_{ges} = \sum_{k=1}^n R_k$

[R] Parallel: $\frac{1}{R_{ges}} = \sum_{k=1}^n \frac{1}{R_k}$ $G_{ges} = \sum_{k=1}^n G_k$

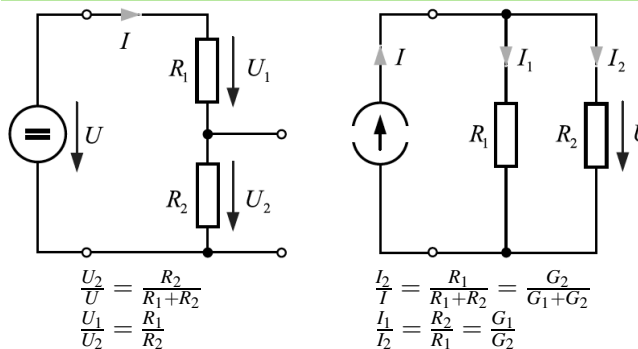
[C] Seriell: $\frac{1}{C_{ges}} = \sum_{k=1}^n \frac{1}{C_k}$

[C] Parallel: $C_{ges} = \sum_{k=1}^n C_k$

[L] Seriell: $L_{ges} = \sum_{k=1}^n L_k$

[L] Parallel: $\frac{1}{L_{ges}} = \sum_{k=1}^n \frac{1}{L_k}$

10.2 Dividers



10.3 Superposition Principle

- Only look at one voltage/current source at a time
- Set all other sources to 0
Current source → open circuit
Voltage source → short circuit
- Add the contributions

10.4 Miscellaneous

- **Gain:** Voltage: $A_{dB} = 20 \log(A)$, $A = 10^{A_{dB}/20}$
Power: Factor 10 instead of 20
- **Gain Bandwidth Product:** $GBP = \omega_p \cdot A(j\omega = 0)$
→ Trade-off between DC-gain and cutoff frequency
- **Simplification:** Often $g_m \gg \frac{1}{r_o}$, $g_m \gg \frac{1}{r_\pi}$
- **Error:** $E = \left| \frac{A_{approx} - A_{ideal}}{A_{ideal}} \right| \cdot 100\%$

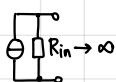
low-pass filter: some Ausdruck like: $A(s) = \frac{R_1}{R_2} \cdot \frac{1}{1+sCR}$
pass-band gain $\frac{R_1}{R_2}$
 ω_c : cutoff frequency = pole frequency $\frac{1}{RC}$

Zusatz

Ideal Voltage Source



Ideal Current Source



Resistances: $\Delta R \rightarrow \infty$: LL, $R \rightarrow 0$ KS

$$R_{ges} = \sum_i R_i$$

$$\frac{1}{R_{ges}} = \sum_i \frac{1}{R_i} \quad \text{Wenn } 2: R_{ges} = \frac{R_1 R_2}{R_1 + R_2}$$

in EC: "neglect R" means $R \rightarrow \infty$ if $R_1 \ll R_2: R_1 \parallel R_2 \approx R_1$

Capacitances:

@ DC $\rightarrow$ kein Strom fließt durch! $i_C = C \cdot \frac{dU_C}{dt}$

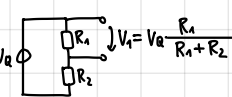
@ AC $\rightarrow Z_C = \frac{1}{j\omega C} = \frac{1}{sC}$

$C_{\infty} \rightarrow$ open @ DC, short @ AC

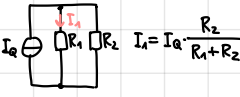
$$\frac{1}{C_{ges}} = \sum_i \frac{1}{C_i} \quad \text{in EC: } C_{ges} = \sum_i C_i$$

Impedances: $Z_L = j\omega L$, $U_L = L \cdot \frac{di_L}{dt}$

Voltage divider:



Current divider:



Transconductance of the circuit: $g_s = \frac{i_{out}}{v_{in}}$

Transconductance of the transistor: g_m

Output Impedance: $\frac{v_{out}}{i_{out}} = r_{out}$

Voltage gain: $\frac{v_{out}}{v_{in}}$

Input Resistance:

"Operating point" means: bestimme voltages / currents s.t. component is in operating ($\hat{=}$ active) mode!
 $\rightarrow$ "saturation" for MOSFET, "active" for BJT!
 meistens eine Größe gegeben (z.B. V_{out}) die erfüllt sein muss.

Voltage gain: $A_V = \frac{v_{out}}{v_{in}}$

Resonance: (may appear in Systems with two or more poles).

$$G(s) = \frac{K\omega_n^2}{s^2 + 2\zeta\omega_n s + \omega_n^2}$$

ω_n : Natural Frequency
 ζ : damping ratio
 K : gain

frequency response: $G(j\omega) = \frac{K\omega_n^2}{(\omega_n^2 - \omega^2 + j(2\zeta\omega_n\omega))}$

$$|G(j\omega)| = \frac{K\omega_n^2}{\sqrt{(\omega_n^2 - \omega^2)^2 + (2\zeta\omega_n\omega)^2}}$$

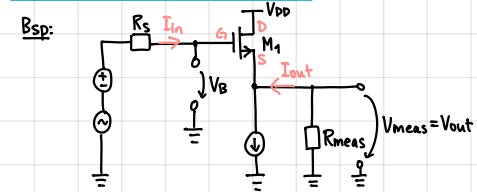
$$\angle G(j\omega) = -\arctan\left(\frac{2\zeta\omega_n\omega}{\omega_n^2 - \omega^2}\right)$$

- For Stability, need $\zeta > 0$
- For $\zeta > 1$ poles real (overdamped system)
- For $\zeta = 1$ poles real & equal (critical damping)
- For $0 < \zeta < 1$ poles complex (under-damped system)
- For $\zeta = 0$ poles imaginary (undamped system)
- For $\zeta \approx 1/\sqrt{2}$ magnitude bode plot decreasing in ω
- For $0 \leq \zeta < 1/\sqrt{2}$ magnitude bode plot has a max. at $\omega = \omega_n \sqrt{1 - 2\zeta^2}$ and $|G(j\omega)| = \frac{K}{2\zeta\sqrt{4 - \zeta^2}}$

Resolution: The smallest increment of measurement, movement or other output that a machine, instrument, or component is capable of making.

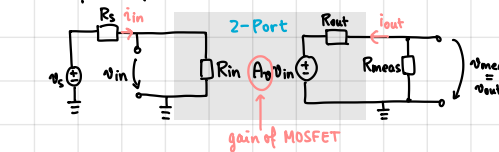
Bsp: $V_{res,s} = 50 \cdot V_{res,meas}$ means the output ($\hat{=}$ measuring device) can only "see" jumps of $n \cdot 50 \text{ mV}$, $n \in \mathbb{N}$ at the sensor! D.h. if V_s changes by e.B. $\pm 25 \text{ mV}$, we won't notice it. $\rightarrow$ Bad resolution!
 $\rightarrow$ sensor "feels" precise changes, but measurement device (bzw. we) won't be able to make use of it! $\rightarrow$ Sol.: smash in MOSFETs!

2-port replacement of an amplifier:



1) Calculate $R_{in} = \frac{v_{in}}{i_{in}}$, $R_{out} = \frac{v_{out}}{i_{out}}$ in SSE.

2) 2-port replacement:



Frequency assumptions:

$$r_o \gg R_L$$

$$g_m r_o \gg 1$$

$$R_o \gg r_{\pi}$$

$$\frac{1}{r_{\pi}} \ll g_m$$

Zehnerpotenzen:

m	10^{-3}	k	10^3
μ	10^{-6}	M	10^6
n	10^{-9}	G	10^9
p	10^{-12}	T	10^{12}
f	10^{-15}	P	10^{15}
		E	10^{18}

Poles of a transfer function:

Dort wo $Im = Re$ im Nenner. (oder einfach dort wo Nenner 0 wird, und s einfach mit w ersetzen.)

Poles angeben als ω , "frequencies" angeben als f .

Grenzfrequenz $\hat{=}$ Eckfrequenz = cutoff frequency $\Delta[\omega] = \text{rad/s}$

Impedanz

$$Z_C = \frac{1}{j\omega C} = \frac{1}{sC}$$

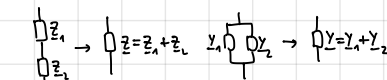
$$Z_L = j\omega L = sL$$

Admittanz:

$$Y_C = \frac{1}{Z_C} = sC$$

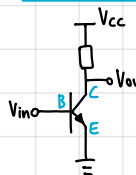
$$Y_L = \frac{1}{Z_L} = \frac{1}{sL}$$

$$s = j\omega$$

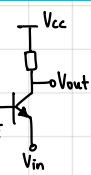


Common $\alpha \rightarrow$ no input/output connected to α .

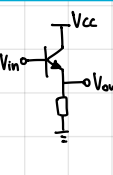
BJT: Common Emitter



Common Base



Common Collector



MOSFETs: Common Gate

Common Source

Common Drain

Transfer function: $A(s) = \dots \rightarrow$ highest order s^n w.o. coeff.!

Filter 2. Ordnung: LC-Hochpass/Tiefpass

Resonanzfrequenz $f = \frac{1}{2\pi\sqrt{LC}}$ (hier $Re(Z_1 + Z_2) = 0$, $\psi = 90^\circ$)

$\rightarrow$ denominator of the transfer fct. is in the form $s^2 + s \frac{\omega_0}{Q} + \omega_0^2$

Phasenverschiebung
 Resonanzf.
 Güte (quality)

Strom fließt nur, wenn NW geschlossen ist!

Why is it favourable to have a high CMRR for a differential circuit:

In differential circuits, the signal of interest that one wants to have amplified is symmetric, i.e. $v_{i+} = -v_{i-}$. Unwanted/parasitic signals usually affect both v_{i+} and v_{i-} equally and with the same polarity, consequently they appear as common mode signals at the input of the amplifier. Therefore, common mode signals must be amplified with the smallest gain or the highest CMRR possible. Supply or ground variations as interference coupling from the mains are examples of unwanted common mode signals.

Capacitor:

The voltage when charging a capacitor with a voltage source and a resistor (step response of a 1. order System) is:

$$v_C(t) = V_0(1 - e^{-t/\tau}), \quad \tau = RC$$

generer: $v_C(t) = v_C(t \rightarrow \infty) - [v_C(t \rightarrow \infty) - v_C(t = t_0)]e^{-\frac{t-t_0}{RC}}$

and $i_C(t) = \frac{1}{R}[v_C(t \rightarrow \infty) - v_C(t = t_0)]e^{-\frac{t-t_0}{RC}}$

effektive C, bis geladen wird.
 Roffi, R anden kleinem von C. ($\phi \rightarrow \frac{1}{2} \phi \rightarrow 1$)

Induktivität: $v_L(t) = R[i_L(t \rightarrow \infty) - i_L(t = t_0)]e^{-\frac{t-t_0}{L/R}}$

$$i_L(t) = i_L(t \rightarrow \infty) - [i_L(t \rightarrow \infty) - i_L(t = t_0)]e^{-\frac{t-t_0}{L/R}}$$

Laplace Transform, properties

- Linearity: $\mathcal{L}\{\alpha f(t) + \beta g(t)\} = \alpha \mathcal{L}\{f(t)\} + \beta \mathcal{L}\{g(t)\} = \alpha F(s) + \beta G(s)$
- s-shift: $\mathcal{L}\{e^{-at} f(t)\} = F(s+a)$
- Time derivative: $\mathcal{L}\{\frac{d}{dt} f(t)\} = sF(s) - f(0)$, $\mathcal{L}\{\frac{d^2}{dt^2} f(t)\} = s^2 F(s) - s f(0) - f'(0)$
- Convolution: $\mathcal{L}\{f * g(t)\} = F(s) \cdot G(s)$ and $\mathcal{L}\{f(t) \cdot g(t)\} = (F * G)(s)$

Some important Laplace Transforms:

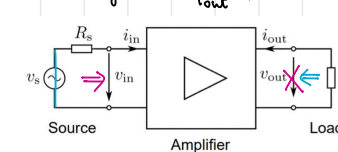
$$\delta(t) \rightarrow 1, \quad \sin(\omega t) \rightarrow \frac{\omega}{s^2 + \omega^2}$$

$$H(s) \rightarrow \frac{1}{s}, \quad \cos(\omega t) \rightarrow \frac{s}{s^2 + \omega^2}$$

$$e^{-at} \rightarrow \frac{1}{s+a}$$

When to neglect what:

$\rightarrow$ When calculating $r_{out} = \frac{v_{out}}{i_{out}}$, set $v_{in} = 0$



Input Impedance: $Z_{in} = \frac{v_{in}}{i_{in}}$

with $v_{out} = 0$

Output Impedance: $Z_{out} = \frac{v_{out}}{i_{out}}$

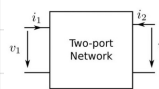
with $v_s = 0$

Voltage Gain: $A_V = \frac{v_{out}}{v_s}$, $\rightarrow$ assume no load

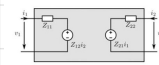
Current Gain: $A_I = \frac{i_{out}}{i_s}$

with $R_L = 0$

$$A_s = \frac{v_{out}}{v_{in}}$$

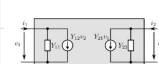


- General linear two-port network
- The (linear) relationship among i_1, v_1, i_2, v_2 can always be expressed in terms of 4 parameters but they can take different forms



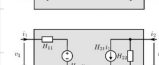
$$\begin{bmatrix} v_1 \\ v_2 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{bmatrix} \begin{bmatrix} i_1 \\ i_2 \end{bmatrix}$$

Impedance



$$\begin{bmatrix} i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

Admittance



$$\begin{bmatrix} v_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} H_{11} & H_{12} \\ H_{21} & H_{22} \end{bmatrix} \begin{bmatrix} i_1 \\ v_2 \end{bmatrix}$$

Hybrid

Einheiten:

$$[U] = V, \quad [L] = H$$

$$[I] = A, \quad [p] = \Omega m$$

$$[R] = \Omega, \quad [v] = Sm^{-1}$$

$$[C] = F$$

Spannungsquelle off $\Rightarrow$ KS

Stromquelle off $\Rightarrow$ LL

"Keine Spannung" $\neq$ KS i.A.! (d.h. heisst nicht dass Strom einfach so durchfließen kann. ϕ gleich heisst nicht Stromfluss!
 (Virtueller KS $\rightarrow$ nur wenn kein Stromfluss!)

Bode-Diagramm

Gezeichnet wird von kleinen zu grossen Frequenzen, d.h. links nach rechts / Darstellung in dB-Skala $\rightarrow F(\omega)[dB] = 20 \log_{10}(F(\omega))$

1. Faktorisieren der Funktion: $F_{ges}(j\omega) = K_0 (j\omega)^r \underbrace{F_1(j\omega) \cdot F_2(j\omega) \cdot \dots \cdot F_n(j\omega)}_{F_{ges}^*(j\omega)}$

⚠ Grenzfrequenz $\hat{=}$ Eckfrequenz $\hat{=}$ Cutoff-frequency (all same!)
 ⚠ Amplitudenplot: $20dB \cdot \log_{10}(|G(j\omega)|)$

Teilsysteme $F_i(j\omega)$ in Standardform

$F_i(j\omega) = 1 + j\omega T_{n,i}$	Steigung +20dB/Dekade	Phase +90°
$F_i(j\omega) = \frac{1}{1+j\omega T_{p,i}}$	Steigung -20dB/Dekade	Phase -90°
$F_i(j\omega) = 1 + 2d_i T_{n,i}(j\omega) + (j\omega)^2 T_{n,i}^2$	Steigung +40dB/Dekade Bedingung: $d_i \leq 1$, sonst Polynom mit 2 reellen Nullstellen	Phase +180°
$F_i(j\omega) = \frac{1}{1+2d_i T_{p,i}(j\omega) + (j\omega)^2 T_{p,i}^2}$	Steigung -40dB/Dekade Bedingung: $d_i \leq 1$, sonst Polynom mit 2 reellen Polstellen	Phase -180°

2. Teilsysteme nach aufsteigenden Eckfrequenzen $\omega_i = 1/T_{n,i}$ bzw. $\omega_i = 1/T_{p,i}$ sortieren (ω_1 = kleinste Eckfrequenz)

Amplitudengang (doppellogarithmische Darstellung)

3. Startpunkt: $\omega_1 / F_{dB}(\omega_1) = 20 \log_{10}(|K_0 F_{ges}^*(0)| \cdot \omega_1^r)$
4. Startpunkt nach links: Gerade mit Steigung $r \cdot 20dB/Dekade$ (Für $r = 0$ waagerechte Gerade)
5. Startpunkt nach rechts: Geradensegmente von einer Eckfrequenz bis zur nächst höheren Eckfrequenz. Bei jeder Eckfrequenz ω_i **ändert Amplitudengang Steigung je nach Teilsystem, das zur Eckfrequenz gehört** (s.o.).
 Mehrfache Pol-/Nullstellen: Steigungsänderung mehrfach nehmen. *Immer zur jetzigen Steigung dazugaddieren!*
6. Annäherung: Ecken bei Eckfrequenz noch um $\pm 3dB$ bzw. Vielfachen davon bei mehrfachen Pol-/Nullstellen abrunden ($+n \cdot 3dB$ bei konvexem / $-n \cdot 3dB$ bei konkavem Verlauf). Dies gilt nur für konjugiert komplexe Pole mit Dämpfung $d_i > 1/2$.

Falls $d_i < 1/2$:
 - Resonanzüberhöhung bei ω_i um $-20 \log_{10}(2d_i)dB$ oberhalb Geradennäherung
 - Amplitude: $|F(j\omega_i)| = \frac{1}{2d_i}$
 - Resonanzkreisfrequenz $\omega_r = \omega_i \sqrt{1 - d_i^2} \Rightarrow$ Punkt um $-20 \log_{10}(2d_i \sqrt{1 - d_i^2})dB$ oberhalb Geradennäherung

Phasengang (logarithmische x-Achse)

7. Startfrequenz ω_1 nach links:

$$\varphi(0) = \begin{cases} r \cdot 90^\circ & \text{falls } K_0 F_{ges}^*(0) > 0 \\ -180^\circ + r \cdot 90^\circ & \text{falls } K_0 F_{ges}^*(0) < 0 \end{cases}$$

8. Startpunkt nach rechts: Phase ändert sich bei jeder Eckfrequenz ω_i je nach Teilsystem (s.o.).

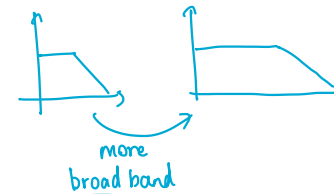
9. Annäherung: Glieder 1. Ordnung - **Phasenverlauf mit $\pm 45^\circ/Dekade$ zwischen $0.1\omega_i$ und $10\omega_i$**

Konjugiert komplexe Pole: Phasenänderung bei Eckfrequenz um so steiler, je kleiner d_i

Phasengang für Teilsystem $F_i(j\omega)$ ist punktsymmetrisch zu dazugehörigen Eckfrequenz ω_i .

10. $\omega \rightarrow \infty$: Phase φ_{ges} strebt gegen $(m - n) \cdot 90^\circ$ (n Grad Nenner- & m Grad Zählerpolynom).

Tiefpass:



Bei Filter (bzw. Glieder) 1. Ordnung:

Ⓢ Grenzfrequenz:

↳ $|Re(\text{den. von } G(s))| = |Im(\text{den. von } G(s))|$

↳ Phasenverschiebung $\varphi = 45^\circ$

↳ Ausgangsamplitude = $\frac{1}{\sqrt{2}}$ Eingangsamplitude

↳ da $20dB \cdot \log_{10}(1/\sqrt{2}) = -3dB \rightarrow$ also called **-3dB-Eckfrequenz!**

Bode Way:

	$\phi(\omega=0)$	$\phi(\omega=\infty)$	$\Delta M(\omega=0)$	$\Delta M(\omega=\infty)$
Integrator $\frac{1}{s}$	-90°	-90°	-20dB	-20dB
Differentiator s	90°	90°	20dB	20dB
Negative Pole $\frac{1}{s+a}$	0	-90°	0	-20dB
Positive Pole $\frac{s}{s+a}$	0	90°	0	-20dB
Negative zero $(s+a)$	0	90°	0	20dB
Positive zero $(s-a)$	0	-90°	0	20dB

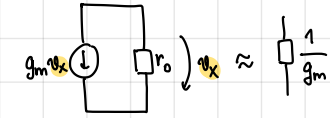
⚠ Poles of Transferfunction: Dort wo $Im = Re$ im Denominator. (oder einfach dort wo $den=0$, mit $s=j\omega$ (s mit ω "ersetzen")

First order **high pass** transfer function: $\beta \frac{s\omega_c}{1+s\omega_c}$ $\omega_c = \frac{1}{RC}$ $\omega_c = \frac{1}{RC}$ $\omega_c = \frac{1}{RC}$
 Passband gain

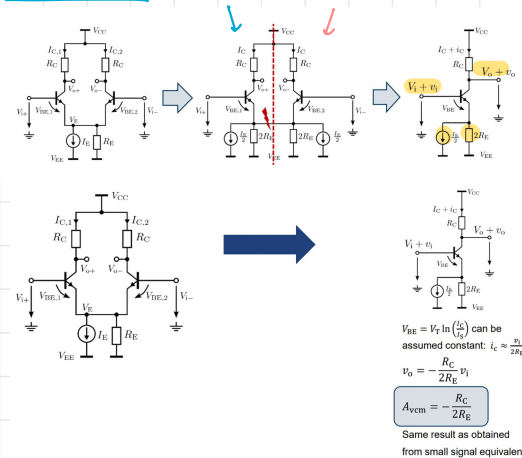
First order **low pass** transfer function: $\frac{1}{1+s\omega_c}$

When asked nach Poles: angeben als ω (rad/s)

Simplifying SSE:



Half-Circuit:



Bsp.

